

FIG.-1

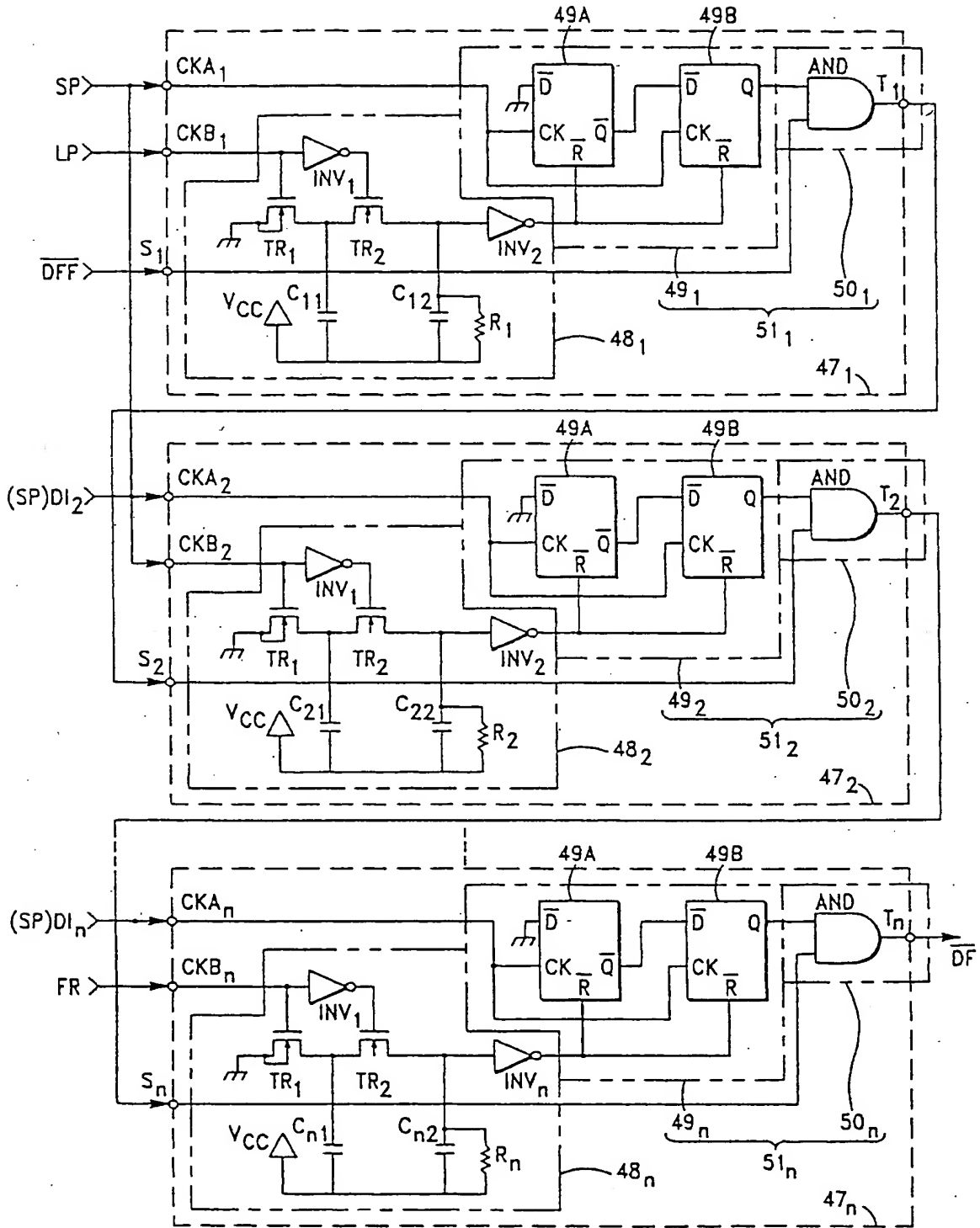


FIG.-2

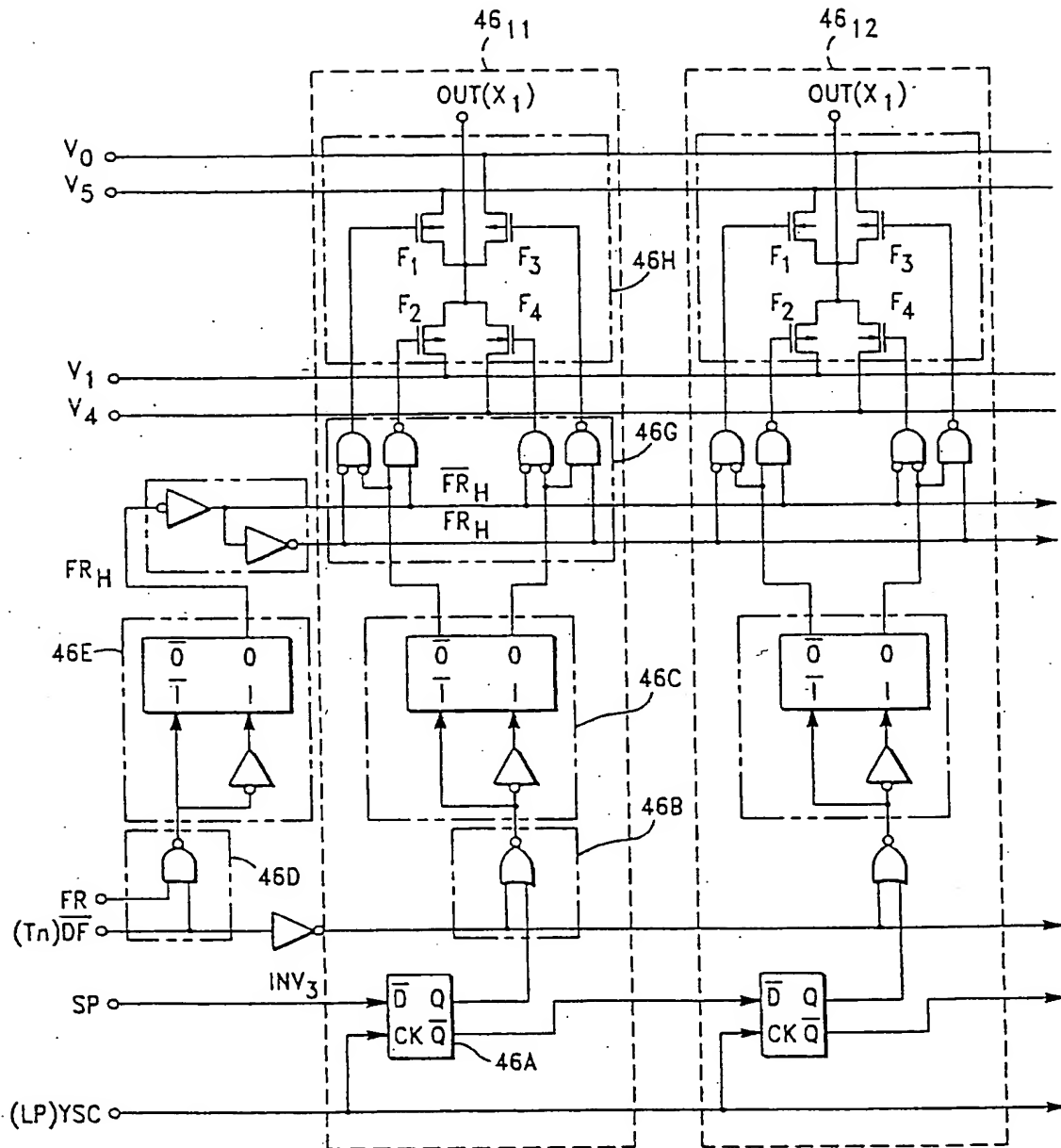


FIG.—3

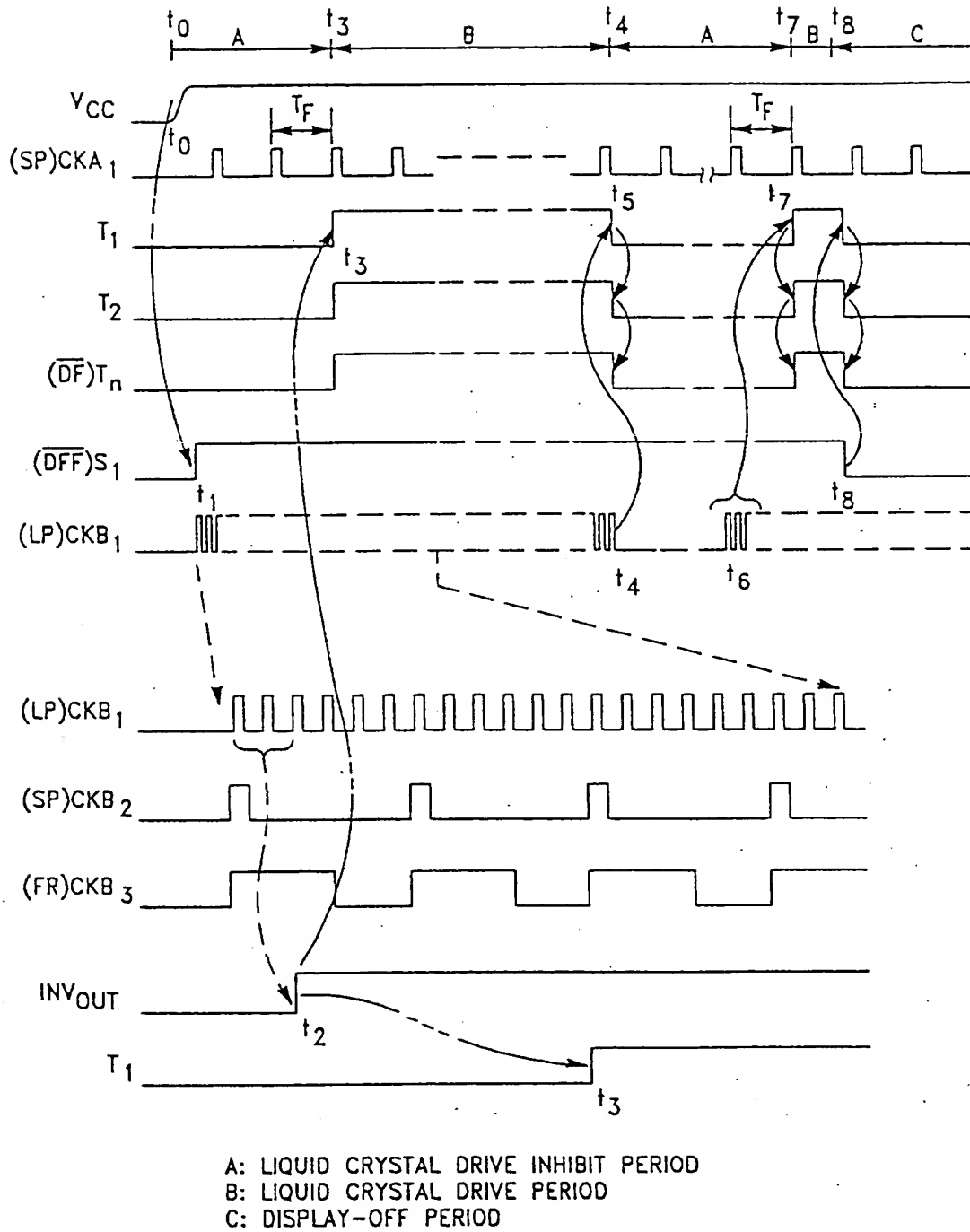


FIG.-4

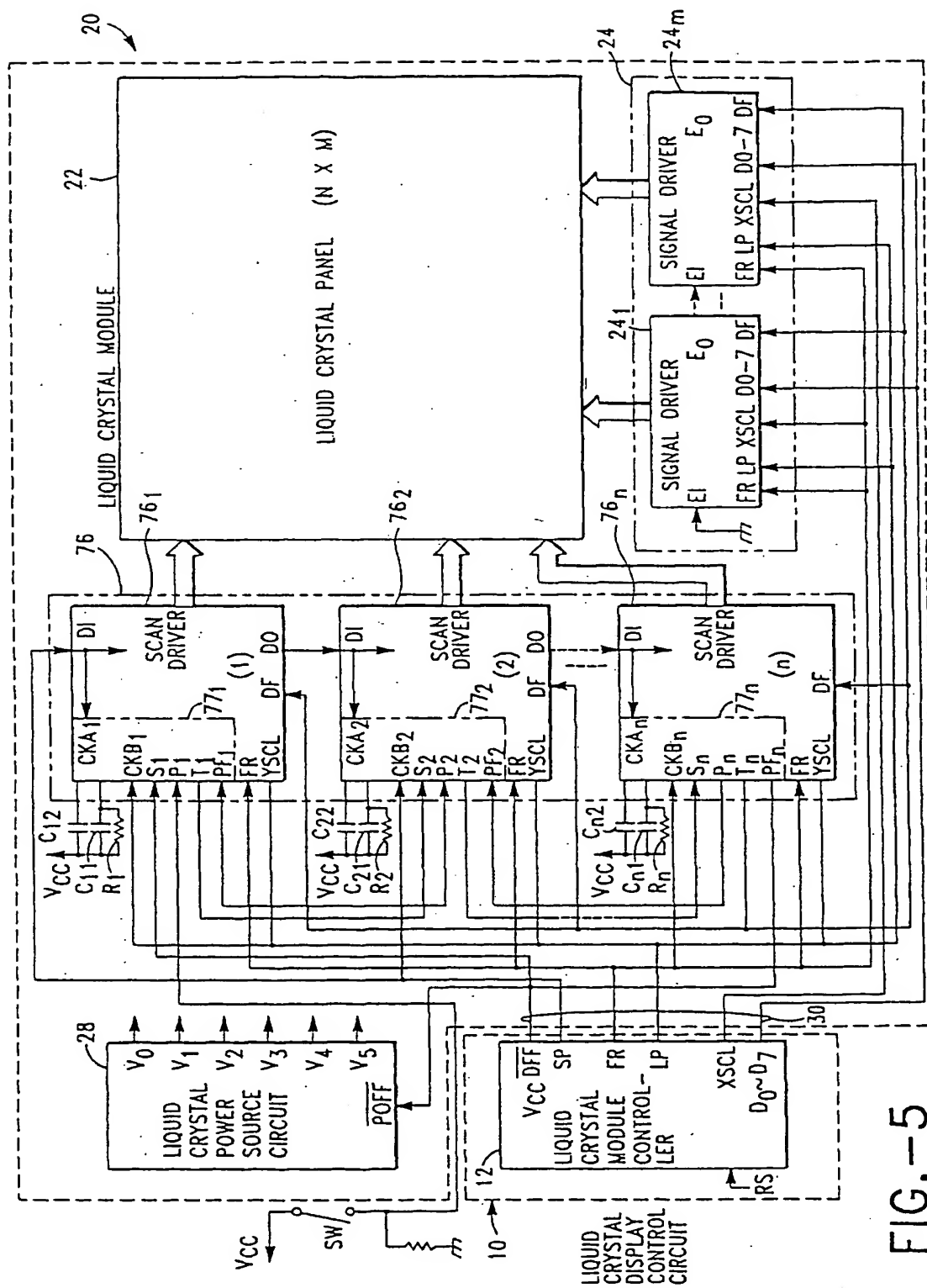


FIG.-5

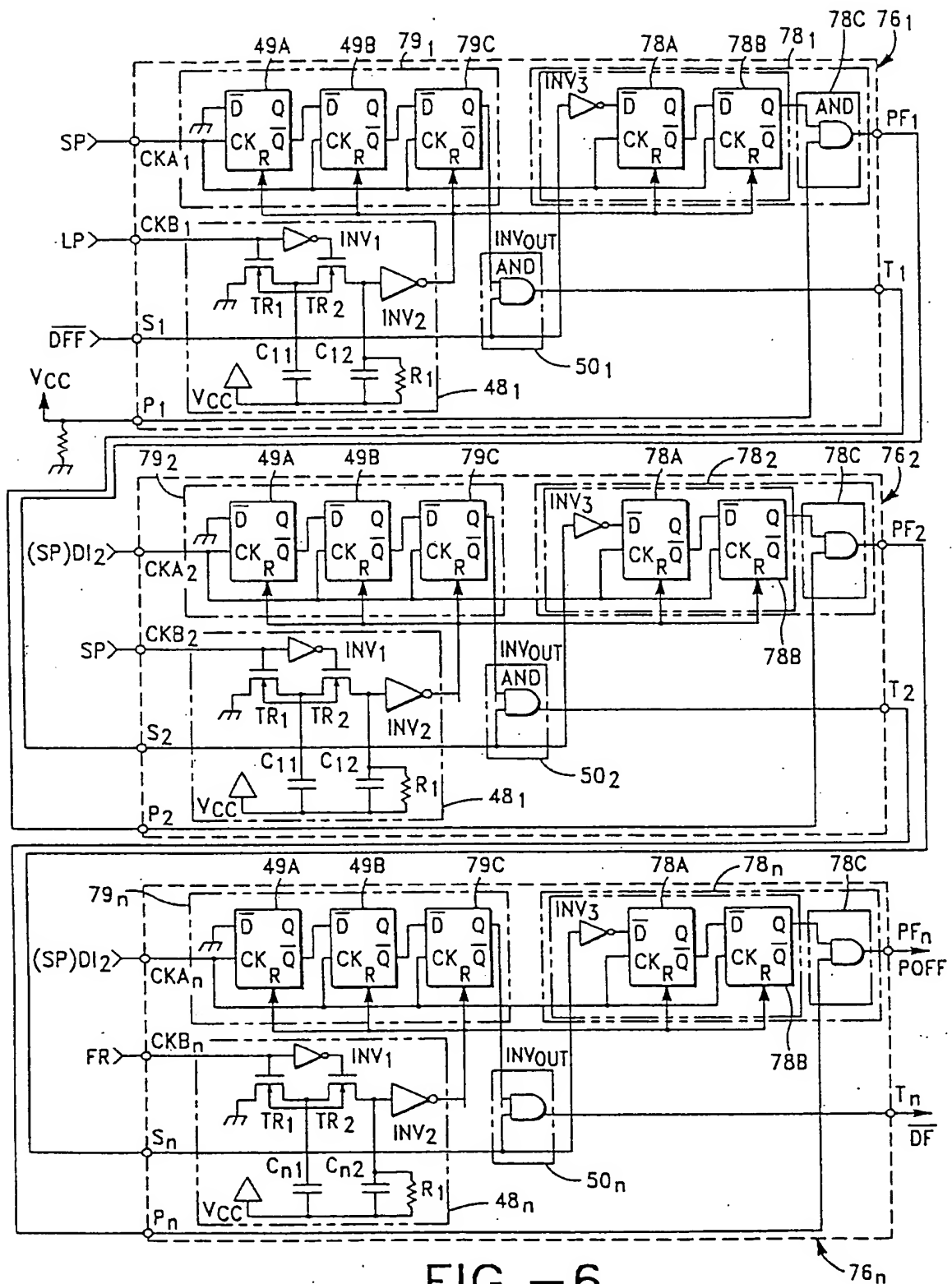


FIG.—6

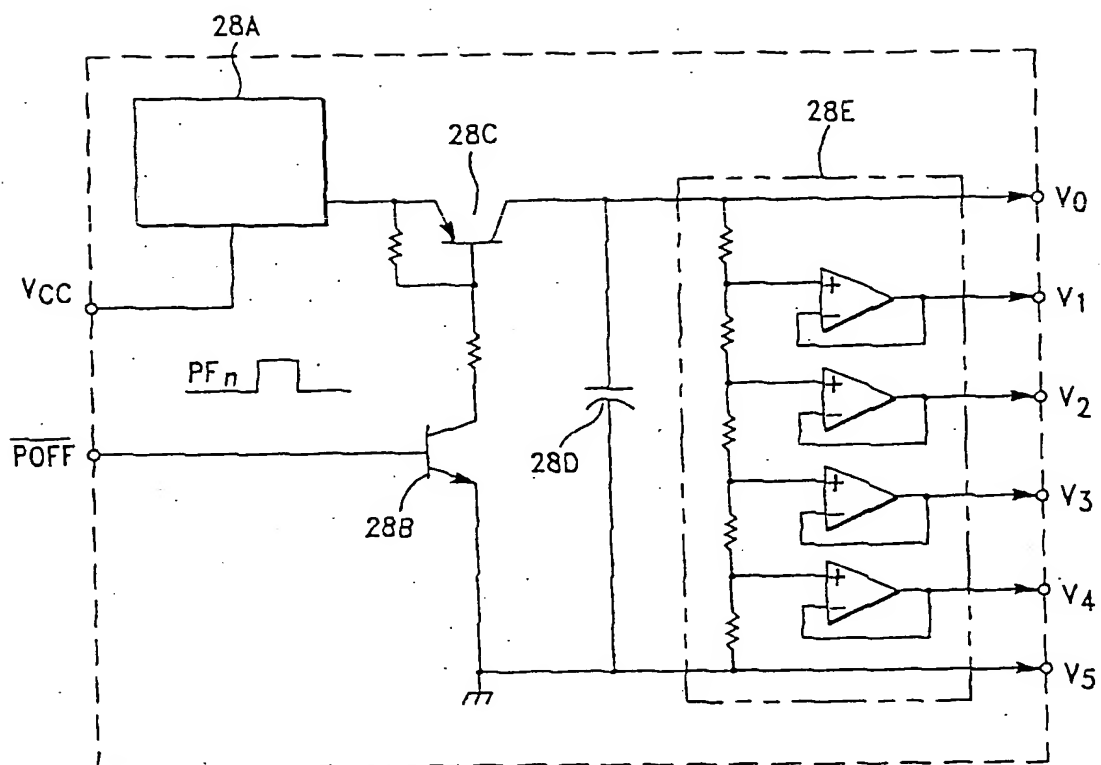
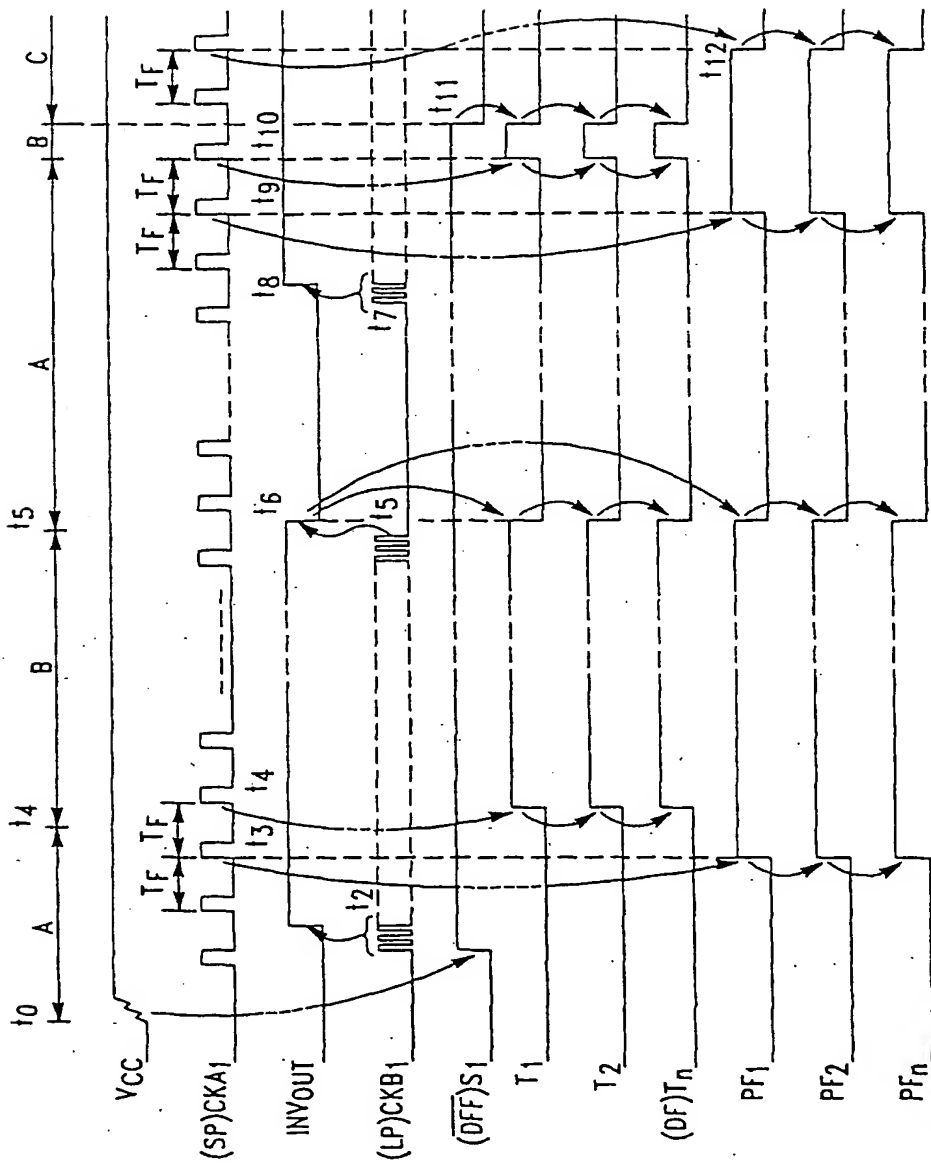


FIG.-7  
(PRIOR ART)



A: LIQUID CRYSTAL DRIVE INHIBIT CIRCUIT  
B: LIQUID CRYSTAL DRIVE PERIOD  
C: DISPLAY-OFF PERIOD

FIG.-8



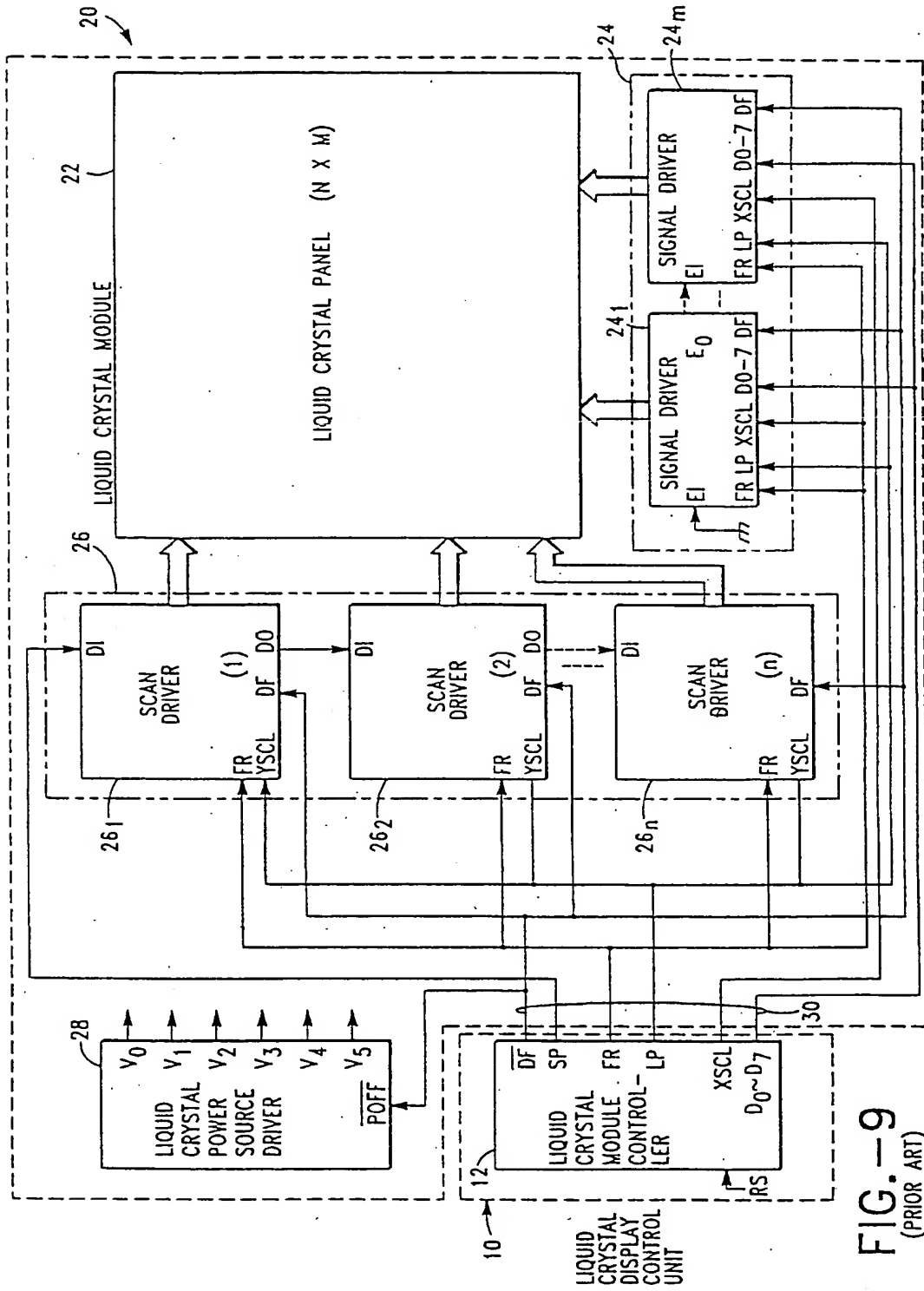


FIG. -9  
(PRIOR ART)